

InSync Technologies – EFS Product Brochure

■ Mission

To revolutionize semiconductor design by delivering executable, specification-driven automation that bridges architecture to silicon.

■ Key EFS Features

- Modular and reusable flow specifications
- RTL, stimulus, and checker generation
- Coverage models for functional and protocol-level design

■ What You Get

- Executable flow specs
- RTL outputs
- Auto-checkers
- Validations aligned to architecture

■ SMART SoC/IP Development

- S - Simple flow modeling
- M - Modular and reusable
- A - Analyzable early
- R - Reusable across tools
- T - Testable from spec to silicon

■ Toolchain Integration

Works seamlessly with popular EDA tools and formats (PlantUML, Verilog, CSV, JSON).

■ Reusability & Efficiency

Accelerate project timelines and reduce rework by reusing validated specs and auto-generated assets.